

IDS Submitted: November 13, 2009

<b>Form 1449 (Modified)</b>	<b>Serial No.: 10/037,593</b>
	<b>Atty. Docket No.: GLVE0157</b>
<b>Information Disclosure</b>	<b>Applicants: Matthew P. KULIG, et al.</b>
<b>Statement By Applicant</b>	<b>Art Unit: 2457</b>
	<b>Confirmation No.: 6376</b>
<b>(Use Several Sheets if Necessary)</b>	<b>Filing Date: October 19, 2001</b>

**U.S. Patent Documents**

Examiner Initials	No.	Patent No.	Issue Date	Patentee

**Published U.S. Patent Application**

Examiner Initials	No.	Publication No.	Publication Date	Applicant	Priority Date

**Foreign Patent or Published Foreign Patent Application**

Examiner Initials	No.	Document No.	Publication Date	Applicant

**Non-Patent Literature Documents**

Examiner Initials	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	OK	Ramesh, et al.; "Automatic Selection of Tuning Parameters for Feature Extraction Sequences"; June 21-23, 1994; IEEE; pp. 672-677; USA.
	OL	Niewczas, et al.; "A Pattern Matching Algorithm for Verification and Analysis of Very Large IC Layouts"; April 6-8, 1998; ACM; pp. 129-143; Monterey, California.

Examiner's Signature: /Barbara Burgess/Date: 01/13/2010

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.